## 國立虎尾科技大學109學年度電機工程系碩士班課程規劃表

Curriculum of the master program of the department of electrical engineering, National Formosa University (Academic year 2020)

at pre	碩士班一年級/1st academic year	⊦ /1st	T/Ond	碩士班二年級/2nd academic ye		
at pre		上/1st	下/2nd	¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬¬	上/1st	下/2nd
お口で	科目/course	semester	semester	+ 科目/course	semester	semester
古匹		學分數/時數 credits/hours	學分數/時數 credits/hours		學分數/時數 credits/hours	學分數/時數 credits/hours
	夏研究(一)/Research Project(1)	0/2		碩士論文(一)/Thesis(1)	3/0	
必修 /Required	及討論(一)/Seminar(1)	0/2		碩士論文(二)/Thesis(2)		3/0
Courses 專題	夏研究(二)/Research Project(2)		0/2			
書報	及討論(二)/Seminar(2)		0/2			
能源	原轉換/Energy Conversion	3/3		電力系統穩定度分析/Power System Stability Analysis	3/3	
高等	享電機機械/Advanced Electrical Machinery	3/3		不斷電電源系統設計/UPS System Design	3/3	
電力	为系統運轉與控制/Power System Operation and Control	3/3		強健控制/Robust Control	3/3	
切换	與式電源供應器/Switching Mode Power Supply	3/3		視訊通訊/Video Communication	3/3	
電力	ウ品質/Power Quality	3/3		正交分頻多工/Orthogonal Frequency Division Multiplexing	3/3	
高等	穿電力電子/Advanced Power Electronics	3/3		電磁應用/Electromagnetic Application	3/3	
數位	立訊號處理/Digital Signal Processing	3/3		電子安定器/Electronic Ballasts	3/3	
線性	生系統理論/Linear System Theory	3/3		5G行動通訊技術應用/5G mobile communication technology application	3/3	
模糊	明系統/Fuzzy Systems	3/3		混合訊號積體電路佈局設計/Mixed Signal IC Layout and Design		3/3
FPG.	GA電路設計/FPGA Circuits Design	3/3		校外實習/Practicum Training		3/3
高等	穿數位通訊/Advanced Digital Communications	3/3		5G 物聯網與通訊技術/5G IoT and Communications Technologies		3/3
嵌入	∖式系統/Embedded Systems	3/3		Technologies		
超大	大型積體電路設計/Very Large Scale Integrated Circuits	3/3				
Desig 功因	lgn 图修正電路設計/Power Factor Correction Circuit Design	3/3				
無線	泉網路協定技術實務與應用/Wireless Network Technologies	3/3				
	nciples Protocols and Applications <del>劉姆路/Computer Networks</del>	<del>3/3</del>				
	新網平台應用開發/					
Intern	ernet of Things Application Development Platform	3/3				
智慧 Desig	患生活科技系統設計/Intelligent Living Technology System	3/3				
	支論文寫作/Technical Paper Writing		2/2			
交直	直流馬達驅動/AC/DC Motor Driver		3/3			
選修/ 電力	为轉換器設計實務/Power Converter Design Practice		3/3			
Elective Courses 電子	子電路設計實務/Power Electronics Design Practice		3/3			
	售性濾波器/Adaptive Filtering		3/3			
線性	生控制器設計/Linear Controller Design		3/3			
電腦	甾視覺/Computer Vision		3/3			
非線	泉性系統/Nonlinear System		3/3			
智慧	表型控制/Intelligent Control		3/3			
	言與網路/Communications and Networks		3/3			
	大式系統設計/Embedded System Design		3/3			
_	力電子磁性元件與應用/ gnetic Device and Application of Power Electronics		3/3			
高等	享FPGA系統設計與實務/ vanced FPGA System Design and Practice		3/3			
-	为通訊/Mobile Communications		3/3			
網路	各效能分析與模擬/		3/3			
	work Performance Analysis and Simulations		3/3			
	泉感測網路/Wireless Sensor Networks 應性訊號處理/Adaptive Signal Processing		3/3			
	医性訊號處理/Adaptive Signal Processing		3/3			
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	充應用設計與實務/Application System Design and Practice		3/3			
	家心晶片設計實作/Multicore Chip Design Laboratory		3/3			
	月驅動電路/Lighting Drivers 絲網核心技術與應用/Key Technologies and Applications of		3/3			
IoT			3/3			
	5G行動寬頻協同網路/4G/5G Mobile Broadband laborative Network		3/3			
' '	穿電路理論/Advanced Circuit Theorem		3/3			
ı	慧機器人應用/Applications in Intelligent Robotics F究生最低畢業總學分數為30學分,其中必修包含論文6學。		3/3			

<sup>1.</sup>本系碩士班研究生最低畢業總學分數為30學分,其中必修包含論文6學分、二學期之書報討論及專題研究,選修至少修滿24學分,並須通過碩士學位考試。

This program is commenced from the academic year 2020.

M.S. students in the program must complete at least 30 credits of coursework, including 24 credits elective course, 6 credits of thesis, seminar coursework through 2 semesters and the master degree examination must be passed.

<sup>2.</sup>畢業學分必須包含2學分之「科技論文寫作」;外系選修課至多承認6學分。

The 2 credits technical paper writing coursework is required and students may elect other academic department courses to satisfy up to 6 hours of elective credit.

<sup>3.109</sup>學年度起適用。