虎尾科技大學電機工程系個人資料表

一、基本資料

中文姓名	陳厚銘	英文姓名	Chen Hou-Ming (Last Name) (First Name) (Middle Name)		
國籍	中華民國	性別	■男 □女	出生日期	68 年 2 月
聯絡地址	□□□□□ 雲林縣虎尾鎮文化路 64 號				
聯絡電話	(公).05-6315615				
傳真號碼		E-MAIL	jchen99@nfu.edu.tw		

二、主要學歷

畢／肄業學校	國別	主修學門系所	學位	起訖年月(西元年/月)
國立中興大學	中華民國	電機工程學系	博士	2003 / 9 至 2009/1
				___/___至___/___

三、現職及與專長相關之經歷

指與研究相關之專任職務，請依任職之時間先後順序由最近者往前追溯。

服務機關	服務部門／系所	職稱	起訖年月(西元年/月)
現職： 國立虎尾科技大學	電機系	助理教授	2010 / 2

四、專長 請自行填寫與研究方向有關之學門名稱。

1. 微電子學門	2.	3.	4.

五、研究表現

(一)期刊論文

[1] **Hou-Ming Chen**, Robert C. Chang and Kuang-Hao Lin, "A high-efficiency monolithic dc-dc pfm boost converter with parallel power mos technique," *Advanced VLSI Design Methodologies for Emerging Industrial Multimedia and Communication Applications*, vol. 2013, pp. 1-7, May 2013. (EI)

[2] Kuang-Hao Lin, Jan-Dong Tseng, Wei-Hao Chiu, **Hou-Ming Chen**, and Jun-Hong Weng, "Implementation of synchronization architecture in ban for health-care systems," *International Journal of Electrical Engineering*, vol. 20, no. 3, pp. 91–98, June 2013. (EI)

[3] Kuang-Hao Lin, Meng-Yi Lin, **Hou-Ming Chen** and Yu-Cherng Huung, "A Modified WiMAX Parity-Check Matrix for Low-Density Parity-Check Codes," *Innovative Computing, Information and Control Express Letters*, vol. 6, no. 10, pp. 2551–2556, Oct. 2012. (EI)

[4] Robert C. Chang, **Hou-Ming Chen**, Wang-Cyuan Jheng, Chu-Hsiang Chia, Pui-Sun Lei and Zong-Yui Lin, "Adaptive Sense Current Control for DC-DC Boost Converters to Get Accurate Voltage," *IEICE Trans. on Electronics*, vol.E92-C, no.8, August 2009. (SCI)

[5] Robert C. Chang, **Hou-Ming Chen**, Chu-Hsiang Chia, and Pui-Sun Lei, "An exact current-mode PFM boost converter with dynamic stored energy technique," *IEEE Trans. on Power Electronics*, vol.24, no.4, pp.1129-1134, April 2009. (SCI)

[6] Robert C. Chang, **Hou-Ming Chen** and Po-Jen Huang, "A multiphase-output delay-locked loop with a novel start-controlled phase/frequency," *IEEE Transactions on Circuits and Systems I*, vol.55, pp.2483-2490, November 2008. (SCI)

[7] Robert C. Chang, Lung-Chih Kuo, and **Hou-Ming Chen**, "A low-voltage low-power CMOS phase-locked loop," *Journal of Circuits, Systems, and Computers*, vol.14, no.5, pp.997-1006, October 2005. (SCI)

[8] Mu-Chun Wang, **Hou-Ming Chen**, Chen-Tsun Tsai, Yung-Chen Chen and Liang-Te Lu, "A powerful electrical probing method to detect the kink effect of MOSFET devices," *Journal of Da-Yeh University*, no.1, vol. 13, pp.23-27, June 2004.

[9] Mu-Chun Wang, **Hou-Ming Chen**, Chen-Tsun Tsai, Liang-Te Lu and Yu-Jie Liao, "Low phase-noise CMOS voltage-controlled oscillator for ism band," *Journal of Da-Yeh University*, no.1, vol.13, pp.29-34, June 2004.

[10] Mu-Chun Wang, Chen-Tsun Tsai, **Hou-Ming Chen**, and Yu-Jie Liao, "Detection of gate-oxide integrity in plasma etching process with differential amplifier circuit design," *Journal of Engineering, NCNU*, vol.7, no.1, pp.155-168, June 2003.

[11] Mu-Chun Wang, **Hou-Ming Chen**, Chen-Tsun Tsai and Jin-Hua Hong, "A powerful and sensitive gauge for plasma-process-induced damage in differential amplifier circuit design," *Journal of Da-Yeh University*, no.1, vol. 12, pp.37-41, June 2003.

(二)研討會論文

[1] Kuang-Hao Lin, Tai-Hsuan Yang, Ren-Hao Wu, **Hou-Ming Chen**, and Jan-Dong Tseng, "A Multimedia Game Development System with an Intelligent Mobile and Embedded Platform," *IEEE Asia Pacific Conference on Circuits and Systems* (APCCAS), Kaohsiung, Taiwan, pp. 651–654, Dec. 2012. (EI)

[2] **Hou-Ming Chen**, Robert C. Chang, Pui-Sun Lei, "An exact, high-efficiency pfm dc-dc boost converter with dynamic stored energy controller," *The 15th IEEE International Conference on Electronics, Circuits and Systems*, pp.622-625, Malta, August 2008. (EI)

[3] **Hou-Ming Chen**, Robert C. Chang, Jian-Lin Wu, "A low-voltage integrated current-mode boost converter for portable power supply," *The 14th IEEE International Conference on Electronics, Circuits and Systems*, pp. 1316-1319, Marrakech, Morocco, December 2007 (Best Paper). (EI)

[4] **Hou-Ming Chen**, Robert C. Chang, and Chih-Liang Huang, "Low-voltage zero quiescent current PFM boost converter for portable devices," *IEEE International SOC Conference*, pp. 177-180, Hsinchu, Taiwan, September 2007. (EI)

[5] **Hou-Ming Chen**, Ding-Da Jiang and Robert C. Chang, "A monolithic boost converter with an adaptable current-limited PFM scheme," *2006 IEEE Asia Pacific Conference on Circuits*

and Systems, pp. 662-665, Singapore, December 2006. (EI)

[6] **Hou-Ming Chen**, Chih-Liang Huang, Robert C. Chang, "A new temperature-compensated CMOS bandgap reference circuit for portable applications," *IEEE Intl. Symp. on Circuits and Systems*, pp. 21-24Kos, Greece, May 2006. (EI)

[7] Cheng-Hui Chang, **Hou-Ming Chen**, and Robert C. Chang, "A 2.3v CMOS monolithic, 84% efficiency PFM control dc-dc boost converter for white leds driver ic," *The Sixth International Conference on Power Electronics and Drive Systems,* 833-837, Kuala Lumpur, Malaysia, November 2005. (EI)

[8] Larry Li, J. **Hou-Ming Chen**, Robert C. Chang, "A low jitter delay-locked loop with a realignment duty cycle corrector," *IEEE International SOC Conference,* pp.75-76, Washington DC, September 2005. (EI)

[9] **Hou-Ming Chen** and Robert C. Chang, "Low power and area effective curvature-compensated CMOS bandgap reference," *The 16th VLSI Design/CAD Symposium*, Hua-Lein, Taiwan, August 2005.

[10] P.-J. Huang, **H.-M. Chen**, R. C. Chang, "A novel start-controlled phase/ frequency detector for multiphase-output delay-locked loops," *The Fourth IEEE Asia-Pacific Conference on Advanced System Integrated Circuits*, pp.68-71, Fukuoka, Japan, August 2004. (EI)

[11] M.-C. Wang, **H.-M. Chen**, C.-T. Tsai, L.-T Lu, "Full-CMOS 2.4-ghz low-noise amplifier for ism-band wireless communication," *2004 Cross Strait Tri-regional Radio Science and Wireless Technology Conference*, ppD2-1~D2-5, Taiwan, September 2004.

(三)專利

I. **陳厚銘**、陳冠旻、陳威志、王丞志和曾華慶,"具可調式溫度校正技術之精確能隙參考電路," 中華民國發明專利，101 年 6 月送審。

II. 張振豪、**陳厚銘**,"程式化直流轉直流升壓轉換電路," 中華民國發明專利，第 I355791 號，專利期間:101 年 1 月 1 日~117 年 3 月 31 日。

III. 張振豪、**陳厚銘**,"高速低功率零穩態電流之啟動電路," 中華民國發明專利，第 I307215 號，專利期間:98 年 3 月 1 日~114 年 8 月 14 日。

IV. 張振豪、**陳厚銘**、黃聖紘,"緩衝放大器及其自動歸零裝置," 中華民國發明專利，第 I 294211 號，專利期間:97 年 3 月 1 日~114 年 3 月 24 日。

(四)研究補助

I. 99.08.01~100.07.31 行政院國家科學委員會計畫: NSC 99 - 2218 - E - 150 - 048

II. 100.08.01~101.07.31 行政院國家科學委員會計畫: NSC 100 - 2221 - E - 150 - 073

六、 服務

1. 校內服務

I. 99.08.01~100.07.31 夜電機二甲導師

II. 99.08.01~100.07.31 電機一乙導師

III. 100.08.01~101.07.31 夜電機二甲導師

IV. 101.02.01~102.07.31 課外活動指導組組長

V. 101.02.01~102.07.31 虎科大畢聯會指導老師

VI. 101.08.01~102.07.31 技電二甲導師

VII. 102.02.01~102.07.31 學務處副學務長

VIII. 102.08.01~103.07.31 夜電機二甲導師

IX. 102.08.01~103.07.31 電機系學會指導老師

2. 學術服務

I. 2013 智慧電子應用設計研討會(議程委員)

II. Advanced VLSI Design Methodologies for Emerging Industrial Multimedia and Communication Applications, 2013 Paper Reviewer.

III. 2012 民生電子研討會(Section Chair)

IV. IEEE International Symposium on Circuits and Systems (ISCAS), 2012 Paper Reviewer.

V. IEEE Transactions on Very Large Scale Integration Systems (VLSI), 2011 Paper Reviewer.

VI. IEEE Transactions on Industrial Electronics, 2011 Paper Reviewer.

VII. The 6th Intelligent Living Technology Conference, ILT 2011 (Section Chair)

VIII. AII2011 創新發明研討會 (Section Chair).

IX. 第五屆智慧生活科技研討會(ILT2010), Paper Reviewer.

X. 2008 年第一屆立錡盃電源 IC 設計暨系統應用競賽-電源 IC 設計組 佳作。

The 15th IEEE International Conference on Electronics, Circuits and Systems (ICECS 2008), Paper Reviewer.